Neutron and proton tests of different technologies for the upgrade of cold readout electronics of the ATLAS Hadronic End-cap Calorimeter

M Nagel
Max-Planck-Institut für Physik (Werner-Heisenberg-Institut), Föhringer Ring 6, 80805 Munich, Germany, www.mpp.mpg.de
E-mail: nagel@mpp.mpg.de
on behalf of the HECPAS Collaboration
(IEP Košice, Univ. of Montréal, MPI Munich, IEAP Prague, NPI Rež)

Abstract. The expected increase of total integrated luminosity by a factor ten at the HL-LHC compared to the design goals for LHC essentially eliminates the safety factor for radiation hardness realized at the current cold amplifiers of the ATLAS Hadronic End-cap Calorimeter (HEC). New more radiation hard technologies have been studied: SiGe bipolar, Si CMOS FET and GaAs FET transistors have been irradiated with neutrons up to an integrated fluence of $2.2 \cdot 10^{16} \text{n/cm}^2$ and with 200 MeV protons up to an integrated fluence of $2.6 \cdot 10^{14} \text{p/cm}^2$. Comparisons of transistor parameters such as the gain for both types of irradiations are presented.

1. The ATLAS Hadronic End-cap Calorimeter

The hadronic end-cap calorimeter (HEC) of the ATLAS experiment [1, 2] at the CERN Large Hadron Collider (LHC) is a copper-liquid argon sampling calorimeter in a flat plate design [3, 4]. The calorimeter provides coverage for hadronic showers in the pseudorapidity range $1.5 < |\eta| < 3.2$. The HEC shares each of the two liquid argon end-cap cryostats with the electromagnetic end-cap (EMEC) and forward (FCAL) calorimeters, and consists of two wheels per end-cap, as illustrated in Figure 1.

A HEC wheel is made of 32 modules, each with 40 liquid argon gaps, which are instrumented with active read-out pads. The signals from the read-out pads are sent through short coaxial cables to preamplifier and summing boards (PSB) mounted on the perimeter of the wheels inside the cryostat. The PSB boards carry highly-integrated preamplifier and summing amplifier chips in Gallium-Arsenide (GaAs) technology. The signals from a set of preamplifiers are summed to one output signal, which is transmitted to the cryostat feed-through [5]. Figure 2 shows a PSB board mounted and connected on the perimeter of a HEC wheel.
Figure 1. A liquid Argon end-cap cryostat, containing the hadronic and electromagnetic end-cap calorimeters, and the forward calorimeter [2].

Figure 2. A detailed view of a PSB board mounted on the perimeter of a HEC wheel [5].
2. Requirements of the HEC cold electronics for the HL-LHC upgrade

The GaAs technology currently employed in the HEC cold electronics has been selected for its excellent high frequency performance, stable operation at cryogenic temperatures, and radiation hardness. The radiation hardness specifications were defined for ten years of operation at the LHC design luminosity of \(10^{34} \text{cm}^{-2} \text{s}^{-1}\), including a safety factor of ten. For the high-luminosity upgrade of the LHC (HL-LHC), the luminosity is foreseen to increase by a factor of 5–10, effectively eliminating the safety factor. The ATLAS collaboration therefore decided to re-examine the radiation hardness of the current HEC cold electronics and of potential alternative technologies [6, 7]. Detailed studies of the expected radiation levels after ten years of running under HL-LHC conditions yielded the following requirements (including a safety factor of 10) for the HEC cold electronics [8]:

- 1 MeV equivalent neutron fluence (NIEL in Si) of \(1.7 \cdot 10^{15} \text{n/cm}^2\)
- Total hadron fluence (SEE) of \(1.8 \cdot 10^{14} \text{h/cm}^2\) with \(E_h > 20 \text{ MeV}\)
- Total ionization dose (TID) of 22 kGy

3. Tests

The neutron irradiation tests were performed at the Fast Neutron Facility at the Nuclear Physics Institute of the ASCR in Rež near Prague, Czech Republic, up to an integrated fluence of \(2.2 \cdot 10^{16} \text{n/cm}^2\) (in terms of the 1 MeV equivalent NIEL in Si described in the next section). A 37 MeV proton beam incident on a D\(_2\)O target created a beam of neutrons with a continuous energy spectrum up to a maximum neutron energy of 34 MeV and a flux density up to \(10^{11} \text{n/cm}^2/\text{s}\) with a \(1/r^2\) decrease in flux density. The proton irradiation test were performed at the Proton Irradiation Facility at the Paul Scherrer Institute in Villigen, Switzerland, with a monoenergetic beam of 200 MeV protons up to an integrated fluence of \(2.6 \cdot 10^{14} \text{p/cm}^2\). The transistors were bonded in ceramic casings and mounted on boards, which were then placed in a standard aluminum frame with 17 mm distance between slots and carefully aligned in the particle beams. The three different transistor technologies being tested were Si CMOS FET in SGB25V 250nm technology from IHP, SiGe Bipolar HBT (IHP SGB25V 250nm and IBM 8WLBiCMOS 130nm), and the GaAs FET currently used in ATLAS, either the Triquint CFH800 250nm transistors themselves or integrated into the HEC BB96 Preamplifiers and Systems. The performance of the transistors was monitored in situ during irradiation with a vector network analyzer recording a full set of S-parameters [9], which were converted to standard transistor parameters using suitable small signal circuit models.

4. Flux determination

A combination of radiation films and radmon diodes, placed at various slot positions along the beam, were used to determine the beam profile and, together with the beam current measurements, the particle flux. The films were exposed to a given beam current for various amounts of time and subsequently scanned. An iterative calibration procedure was used to obtain the dose as a function of net optical density [10], by combining the known relative dose measurements with the absolute normalization obtained from MC simulations and from the radmon diodes.

For the neutron test with its continuous neutron energy distribution, the fluence was scaled to the 1-MeV equivalent neutron fluence in Silicon, employing the non-ionizing energy loss (NIEL) scaling hypothesis [11], independent of the actual technology under irradiation. The divergent nature of the neutron beam emerging from the heavy water target yielded a total fluence per slot given by \(\Phi = 3.22 \cdot 10^{16} \cdot (\text{slot}## + \frac{3}{17})^{-2.11} \text{n/cm}^2\). At PSI in contrast, the almost collimated proton beam together with an offset in the alignment of the boards yielded a basically constant...
fluence for all slots of $\Phi = (2.6 \pm 0.4) \cdot 10^{14} \text{ p/cm}^2$, where the main uncertainty stems from the unknown exact positions of the test structures inside the chip casing.

5. Results
The various transistor parameters were calculated from the measured $S$-parameters using suitable small signal circuit models. The transistor parameters were averaged over a certain frequency range to obtain their mean and rms-values for every set of $S$-parameters. This frequency range extended from 300kHz to 100MHz, unless a certain parameter was unstable at low or high frequencies, in which case appropriate cuts were applied. These averaged transistor values were then used to characterize their behaviour as a function of particle fluence.
Table 1. Loss of gain of various transistor technologies under neutron and proton irradiation [12].

<table>
<thead>
<tr>
<th>Technology</th>
<th>Neutron fluence</th>
<th>Proton fluence</th>
</tr>
</thead>
<tbody>
<tr>
<td>Si CMOS FET</td>
<td>0 %</td>
<td>-11 %</td>
</tr>
<tr>
<td>SiGe Bipolar</td>
<td>-5 %</td>
<td>-7 %</td>
</tr>
<tr>
<td>GaAs FET</td>
<td>-20 %</td>
<td>-17 %</td>
</tr>
</tbody>
</table>

Figure 3 summarizes the results for the proton (top) and the neutron (bottom) tests in terms of the appropriate gain parameters as a function of the corresponding particle fluence up to the required limit, normalized to the corresponding value before irradiation. The various gain parameters are the real part of the transconductance $g_m$ for the FET transistors, the differential current gain $\beta$ for the Bipolar transistors, and in case of the HEC BB96 preamplifiers and systems, where our simple transistor models are not applicable, the transresistance $r_m = \frac{\Delta V_{out}}{\Delta I_{in}} = |S_{21} \cdot Z_{in}|$, which relates the output voltage to the input current, where $Z_{in}$ is the input impedance. Displayed are the sample averages of all the devices of a particular technology under test at each of the two test facilities, that were irradiated up to a given particle fluence; the error bars represent the sample RMS. The relative change of the gain parameter is quoted in Table 1, where we also used a linear extrapolation of the gain parameter up to the required fluence for those devices that did not reach that fluence.

6. Summary

Our first observation is that proton irradiation seems to cause more damage to the electronics than neutron irradiation (at least for the Si CMOS and SiGe Bipolar transistors), despite the fact that an order of magnitude more neutrons than protons are expected to impact the HEC cold electronics. In the case of the GaAs FETs, the gain degradation is roughly the same for both types of irradiation, although a quantitative assessment suffers from the fact that two different gain parameters (transconductance vs. transresistance) are being compared.

It can be concluded that both the Si CMOS FETs and the SiGe Bipolar transistors considered as alternative technologies are more radiation hard than the currently used GaAs FETs. Among the alternative technologies, a preference would be given to the Si CMOS FETs, since the SiGe Bipolar transistors require a stabilization of their operation point, which would lead to a more complex circuitry to meet the same radiation hardness requirements.

The GaAs technology currently used in the HEC cold electronics degrades significantly at the expected radiation levels, but it does not break down yet. The observed level of degradation (~20%) could conceivably be compensated for by some sort of calibration procedure. But before one can come to a decision whether it is safe or not to keep the current HEC cold electronics for HL-LHC conditions, the impact of this level of degradation on the overall HEC performance (e.g. in terms of jet energy resolution) needs to be better understood. In addition to that, a measurement of the performance degradation in the cold is needed. A decision has thus been reached to perform a more detailed study of the current HEC BB96 ASICs, including characterization of the linearity of their response with realistic pulses as they appear in the ATLAS detector, and a detailed simulation of the propagation of the ‘degraded’ signals through the summation, digitization, and reconstruction stages. This, and the measurement of the performance degradation in the cold are both ongoing efforts.
References


