Report

Control of High Power IGBT Modules in the Active Region for Fast Pulsed Power Converters

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Keywords: Pulsed power supply, particle accelerator, IGBT, current control

Abstract

At CERN, fast pulsed power converters are used to supply trapezoidal current in different magnet loads. These converters perform output current regulation by using a high power IGBT module in its ohmic region. This paper presents a new strategy for pulsed current control applications using a specifically designed IGBT driver.

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«Pulsed power supply», «Particle accelerator», «IGBT», «Current control».

Abstract
At CERN, fast pulsed power converters are used to supply trapezoidal current in different magnet loads. These converters perform output current regulation by using a high power IGBT module in its ohmic region. This paper presents a new strategy for pulsed current control applications using a specifically designed IGBT driver.

Introduction
In the framework of particle accelerators, pulsed power converters are widely used to supply different magnet loads. Their main advantages, when compared to DC or programmed converters, are the reduced power consumption, the compactness and the reduced cooling requirement for the magnets. A family of converters, developed at CERN, provides trapezoidal current pulses using a high power IGBT module in its ohmic region to regulate the magnet current. This document will first present the ratings, topology and operating modes of these converters and, afterwards, it will focus on the control of the IGBT module itself. The behaviour of the IGBT in its active region is studied and modelled at different operating points, presenting a large variation in its static gain. The IGBT dynamic behaviour is also investigated, showing a large variation depending on the operating current and voltage. In order to mitigate these phenomena, a specific IGBT driver that operates in voltage control mode is implemented, allowing to obtain a quasi-constant closed loop static gain and the stabilization of the dynamics. Then, the associated control system that regulates the output current is analysed in detail and its digital implementation is described. Finally, experimental results are presented, pointing out the advantages of the proposed IGBT driver when compared with an open loop control one.

Converter Parameters, Topology and Operation Principle
At CERN, a type of converter has been developed to supply various magnets loads, mainly quadrupoles, with different operating currents. In these applications, the converter output current is a trapezoidal waveform, which allows short rise and fall times and precision current during the flat-top. The converter basic specifications are given in Table 1.
The converter topology is based on a DC voltage source used to ramp the magnet current and a linear stage to control the current during the flat-top. This ‘non-classical’ topology has been adopted due to the parameters of the magnet loads that have to be powered. In these pulsed applications, the loads parameters are such as $\frac{L}{\frac{di}{dt}} >> R_i$. For the loads considered, the inductive part is usually in the range of a few hundred $\mu$H up to some mH. For this type of magnets, the use of switch-mode power converters based on well-known topologies does not allow to obtain simultaneously a very low ripple current during the flat-top and short rise and fall times. Some additional topologies [1, 2] have been developed specifically for these particular fast pulsed operating modes but their complexity is often not well adapted for low power applications. The converter topology is presented in Figure 1.

During the pulse, the operation of the converter can be divided in three phases, depicted in Figure 2: rise time, flat-top and fall time. The converter operation in each phase can be summarized as follows:

- **Rise time**: when the trigger pulse is received, $S1$ and $S4$ are switched on and $S5$, the high power IGBT module, is in saturation mode. The output current ramps with a slope given by $Vc/Lm$ if the resistive part of the load is neglected.

- **Flat-top**: when the magnet current reaches its reference value $Im_{ref}$, $S5$ is operated in its active region. The output current of the converter is regulated by controlling $S5$ $V_{CE}$ voltage. The branch composed by $R$ and $D$ is used to clamp the IGBT ($S5$) voltage with the purpose of limiting its collector voltage during transients.

- **Fall time**: the output current is ramped down by the turn-off of $S1$-$S4$ and the turn-on of $S2$-$S3$. The current flows through the anti-parallel diode of $S5$ and the energy stored in the magnet capacitance charging power supply.

### TABLE I

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum output current</td>
<td>350 A</td>
</tr>
<tr>
<td>Maximum output voltage</td>
<td>1000 V</td>
</tr>
<tr>
<td>Maximum flat-top duration</td>
<td>from 300 $\mu$s to 10ms</td>
</tr>
<tr>
<td>Maximum repetition rate</td>
<td>5 Hz</td>
</tr>
<tr>
<td>Current precision during flat-top</td>
<td>1000 ppm</td>
</tr>
<tr>
<td>Input from the mains</td>
<td>230V~</td>
</tr>
</tbody>
</table>

Fig. 1: Pulsed converter topology.
load is recovered in the capacitor bank \( C \). When the output current reaches zero, \( D1 \) and \( D2 \) are turned off and the pulse is terminated.

Typical waveforms are shown in Figure 2. During the flat-top, the voltage drop on \( S5 \) is due to the discharge of the capacitor bank at constant current.

![Fig. 2: Power converter typical waveforms during the current pulse.](image)

**IGBT Model and Closed-loop Driver**

During the flat-top, the IGBT \( S5 \) is operated in its active region in order to regulate the output current. The device selected for this application is a 2400A/1700V INFINEON IGBT module which presents low transient thermal impedance. This parameter allows a limited junction temperature excursion during the flat-top and, accordingly, no significant effect of thermal cycling as regard to the lifetime expectancy of the device. The utilization of high power IGBT modules in linear mode is not usual, although it has already been realized for different applications [3]. In order to study the converter current control system, the IGBT operation in linear mode has to be modelled. An IGBT model [4, 5, 6], where the parasitic inductances are neglected, depicted in Figure 3, has been considered.

![Fig. 3: Linear equivalent IGBT model.](image)

The manufacturer datasheet does not allow to obtain the IGBT linear model parameters for different operating points and experimental measurements are required in order to obtain the static and
dynamical parameters of the equivalent model. Results from performed measurements point out that the IGBT static parameters ($g_m$ and $R_o$) are highly dependent on the collector current as shown in figure 4. The measurements have also shown that the equivalent capacitances ($C_{gc}$ and $C_o$) are significantly varying depending on the collector current and $V_{CE}$ voltage.

Fig. 4: Experimental measurements of the static parameters ($g_m$ and $R_o$) of the IGBT model.

Taking into account these characteristics, a specific driver that linearizes the IGBT gain and dynamical behaviour by operating in voltage closed-loop has been developed and implemented. The IGBT driver architecture is shown in Figure 5.

Fig. 5: IGBT driver architecture.

By using this topology for the IGBT driver, the static gain value is reduced and linearized, as shown in figure 6, allowing an efficient and simpler design of the output current control system.

Fig. 6: Experimental measurements of the IGBT gain in open loop (a) and closed loop driver gain (b).

By controlling the IGBT voltage in closed loop, the overall system gain is significantly reduced allowing less sensitivity to perturbations and increased robustness for the current control system. Furthermore, the closed-loop gain is kept constant even with low IGBT operating currents. Figure 7 shows the implementation of the designed driver installed on the IGBT power module.
Control Strategy

As presented in the previous chapter, the system composed by the IGBT and its linear driver has a static gain quasi-independent of the operating conditions, particularly the IGBT collector current. Concerning the dynamics, the IGBT model shown in Figure 3 has been identified at different operating points, showing large variations of the capacitances involved. Consequently, the proper IGBT dynamics exhibit large variations according to the collector current and voltage. This behaviour is mitigated by the closed loop voltage driver, as illustrated in Figure 8, where different transient step responses for different \( I_C \) currents are shown. Even though the natural frequency seems to remain almost constant, other parameters (damping factor, delay, and specially the gain) still exhibit small variations.

The selected control strategy consists in the identification of an average transfer function and the design of a controller capable of handling the small variations of the plant.

System Identification

From Figure 8, the following average model is identified for the system composed by the IGBT module and its driver:

\[
G_{IGBT+Driver}(s) = \frac{V_{ce}(s)}{V_{ce_ref}(s)} = 100e^{-s \cdot 10^{-6}}
\]

\[
G_{IGBT+Driver}(s) = \frac{100e^{-s \cdot 10^{-6}}}{(1.06 \times 10^{-12})s^2 + (922 \times 10^{-9})s + 1}
\]
If the whole circuit during operation (see Figure 1) is considered, the system direct path transfer function is given by:

\[
G_p(s) = \frac{I_m(s)}{V_{ce_{ref}}(s)} = G_{IGBT+Driver}(s) \cdot G_{Load}(s)
\]

\[
G_p(s) = \frac{100e^{-s \cdot 10^{-6}}}{(1.06 \times 10^{-12})s^2 + (922 \times 10^{-9})s + 1} \cdot \frac{C \cdot s}{L_mCs^2 + R_mCs + 1}
\]

The system presents one zero at the origin and, if the implementation of a classical PID type controller \(G_c(s)\) is considered, the system exhibits a non-zero steady state error for a step input reference. Therefore, the control system requires the implementation of a double integrator in order to achieve a zero steady state error. The effect of adding two integrators is a very high phase shift of 180°. In order to compensate this phase shift, a very important derivative action is required. Though this implementation (i.e. PI²D or PIDI) is theoretically possible, the high derivative action requires a very important filtering in order to achieve a robust and stable system response. Due to the complexity of implementing this filtering, a different regulation strategy has been selected, as explained in the following subsection.

**Regulation Algorithm**

Instead of measuring only the load current, the other state variable, \(V_c\), can also be measured and utilized in the control algorithm. A different plant is then considered, composed only by a simple \(L_m R_m\) load:

\[
G_p(s) = \frac{(V_c(s) - V_{CE}(s))}{V_{ce_{ref}}(s)} \cdot \frac{I_m(s)}{(V_c(s) - V_{CE}(s))}
\]

In this case, the load transfer function is given by:

\[
G_{Load}(s) = \frac{1}{L_m s + R_m}
\]

The desired \(V_{CE}\) voltage is obtained by adding to the actuation of the controller the measurement of \(V_c\) using a feed-forward branch. The adopted control strategy is illustrated in Figure 9.

![Fig. 9: Closed-loop magnet current control system with feedforward.](image)
hereafter. Figure 10 shows the Bode plots of the open-loop system (in green) and the closed-loop one (in blue), considering the load parameters specified in Table 2.

### TABLE II

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_m$</td>
<td>100 mΩ</td>
</tr>
<tr>
<td>$L_m$</td>
<td>700 µH</td>
</tr>
</tbody>
</table>

We can observe how the closed-loop plot flattens the gain of the system, increases its bandwidth and does not present the characteristic peak at the resonant frequency of the open-loop system.

![Open-loop and closed-loop control systems bode plots.](image)

**Digital Implementation and measurements**

The load current measurement is performed using a high precision DCCT with 250kHz bandwidth. The magnet current and the capacitor bank voltage are acquired using two 16-bit ADCs operating at 1MSPS. The analogue actuation is performed using a 14-bit 40MSPS DAC. The control algorithm is mapped into a Spartan 6 FPGA from Xilinx. In order to reduce the transients at the beginning of the pulse and to minimize the magnet current settling time, the initial conditions of the controller are computed according to the load resistance and magnet current reference value before the triggering of the pulse. These initial conditions are then applied at the controller output when the flat-top current value is detected. The output H-bridge is controlled by the FPGA based board that is synchronised with a higher level supervision system that allows the synchronisation with the particle beam and the operation of the accelerator complex.

**Experimental Results**

At CERN, these converters are used to supply different magnet types that are mainly quadrupoles. Their inductance and resistance values exhibit large variations so as their operational current and flat-top duration requirements. The experimental results displayed below are obtained with the load parameters given in Table II.
Figure 11 shows a pulse of 300A and 1ms flat-top duration. In this case, the capacitor charging voltage $V_C$ is set to 600V. The current ramps to 300A in 400µs, and the flat-top settling time is about 100µs. The precision of the current during the flat-top is $4 \times 10^{-4}$ of the reference value.

**Conclusion**

This document has investigated the control of high power IGBT modules in the ohmic region for fast pulsed power applications. The IGBT power module has been modelled at different operating points showing high gain and dynamics variations. Consequently, a specific IGBT driver operating in voltage closed-loop has been implemented, allowing the linearization of the IGBT static parameters and the stabilization of its dynamics. The transfer function of the IGBT module and its closed loop driver has been identified and the associated current control system has been designed to achieve zero steady-state error and a fast transient response. The proposed control system has been implemented using an FPGA based regulation board. Finally, experimental results fulfilling the operational requirements have been presented.

**References**