The ATLAS Semi-Conductor Tracker Operation and Performance

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Abstract

The Semi-Conductor Tracker (SCT) is a silicon strip detector and one of the key precision tracking devices in the Inner Detector of the ATLAS experiment at the CERN Large Hadron Collider (LHC). The SCT was installed and commissioned within ATLAS in 2007, and has been has been used to exploit fully the physics potential of the LHC since the first proton-proton collisions at 7 TeV were delivered in 2009. In this paper, its operational status throughout data taking up to the end of 2011 is presented, and its tracking performance is reviewed.

Keywords: SCT, ATLAS, Silicon, Microstrip, Tracking, Radiation Damage

1. Introduction

The ATLAS Inner Detector (ID) is responsible for particle tracking in ATLAS and employs three different detector technologies: a pixel silicon detector closest to the interaction point, the SCT micro-strip silicon detector, and a Transition Radiation Tracker. The overall ID is 5.6m long and 2.1m in diameter, and resides in a 2 T axial magnetic field.

Figure 1: Geometrical layout of the SCT

Figure 1 illustrates the geometrical layout of the SCT, which was designed to ensure energetic particles pass through at least 4 silicon layers in the acceptance region. Closest to the interaction point are four concentric cylinders covering the pseudorapidity range $|\eta| <1.1$-1.4 and radii 30-51cm. On each side of the barrels is an end-cap comprising nine disks covering pseudorapidity range 1.1-1.4 $|\eta| <2.5$ and radii extending to 56cm. The SCT comprises 61m$^2$ of silicon sensors with 6.3 million readout channels. It operates in a near zero humidity nitrogen environment and is cooled nominally to -7$^\circ$C by a $C_2F_8$ evaporative cooling system, to suppress reverse annealing arising from radiation damage.

1.1. The SCT sensors

The silicon sensors[1] are all 285$\mu$m thick single-sided p-on-n devices, with 768 AC-coupled micro-strips. There are 8448 identical rectangular shaped barrel sensors with size 64.0x63.6mm and 80$\mu$m strip pitch. The end cap disks use wedge shaped sensors of five slightly different sizes to accommodate the more complex geometry. There are 6944 wedge sensors in total, with strip pitch ranging from 56.9 to 90.4$\mu$m.

1.2. The Silicon Modules

The basic granularity shown in Figure 1 is the silicon module. There are 2112 identical rectangular shaped modules[2] used to construct the barrel cylinders, and 1976 wedge shaped modules[3] of three different sizes used to construct the end-cap disks. Figure 2 shows photos of the end-cap and barrel modules. Modules consist of two pairs of back-to-back sensors, glued to a thermally conductive substrate for mechanical and thermal stability. Pairs on opposite sides are rotated by 40mrad to form a stereo angle in order to enable a resolution measurement in the direction parallel to the strips. The sensor micro-strips are wire bonded across to form effectively 12cm long strips.

Figure 2: The SCT end-cap modules (left three images) and the barrel modules (right)

1.3. ABCD Readout Chips

Readout is via 128-channel ABCD[4] ASICs which are glued to a Cu-polyimide flex circuit hybrid which forms a bridge across the top of the sensors. The ASICs were manufactured using the radiation-hard DMILL technology, and use a binary readout architecture with a 20ns front end shaping time. A
schematic illustrating the ABCD functionality is shown in Figure 3. The signal is shaped and amplified and applied to a comparator with programmable threshold, nominally set to 1fC. The output of the comparator is strobed by the 40MHz LHC clock, and the binary output stored in a 132-deep pipeline which effectively stores the history of the comparator output for every clock cycle. Upon receipt of a trigger from the ATLAS trigger system, the contents of 3 pipeline bins are sampled. The SCT timing is optimised on a module-by-module basis to ensure hits from tracks from collisions result in a 01X occupancy pattern in the 3 sampled bins (no hit in first bin, hit in second bin and optional hit in third). The ABCD registers a ‘hit’ depending on the 3 bin occupancy pattern, which can be configured to ensure optimal efficiency depending on running conditions (eg 01X and X1X for 25ns and 50ns LHC bunch trains respectively, or any hit pattern containing at least one ‘1’ for cosmic rays).

Figure 3: ABCD functionality schematic.

1.4. Optical Links and Redundancy

Communication to and from the modules is by optical links[5], referred to as TX links for transmission of trigger and command signals to the modules, and RX links for the transmission of data back from the modules. There is a single TX link per module, but two RX links for data transmission back from each side of the module. Off-detector and on-detector VCSEL arrays are used to broadcast data transmission for the TX and RX links respectively.

Redundancy options exist for both the TX and RX links, in case of fibre breaks, VCSEL deaths or IPIN diode problems. Redundancy is built into the TX system by having electrical links from one module to its neighbour. If a module loses its TX signal for any reason, an electrical control line can be set which will result in the neighbouring module sending a copy of its TX data to the module with the failed signal, without any impact on operation. A redundancy loop connects 12 adjacent modules in the barrels. For the end caps, half of the disabled modules are due to one cooling loop permanently disabled as a result of an inaccessible leak in that loop. Fortunately this affects one quadrant of one of the outermost

<table>
<thead>
<tr>
<th>Redundancy</th>
<th>Endcaps</th>
<th>Barrel</th>
<th>SCT</th>
<th>Fraction (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>TX</td>
<td>8</td>
<td>20</td>
<td>28</td>
<td>0.68</td>
</tr>
<tr>
<td>RX link 0</td>
<td>35</td>
<td>13</td>
<td>48</td>
<td>1.17</td>
</tr>
<tr>
<td>RX link 1</td>
<td>40</td>
<td>16</td>
<td>56</td>
<td>1.37</td>
</tr>
</tbody>
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A snapshot of use of TX and RX redundancy in the SCT is shown in Table 1. The use of RX redundancy is relatively stable and numbers in the table are predominantly due to link failures during construction and integration of the SCT. The use of TX redundancy has varied widely during operations, mainly due to TX VCSEL deaths as described elsewhere in this paper.

2. Operations

LHC delivered 47pb$^{-1}$ and 5.6fb$^{-1}$ of proton-proton collision data at 7TeV in 2010 and 2011 respectively, as well as two 1 month periods of heavy ion collisions. The SCT has been fully operational throughout all data taking periods. It delivered high quality tracking data for 99.9% and 99.6% of the delivered luminosity in 2010 and 2011 respectively.

2.1. Acceptance

More than 99% of the SCT strips have remained fully functional and available for tracking at all times. Table 2 lists the typical numbers of disabled detector elements; the number of disabled strips (typically due to high noise or un-bonded channels) and non-functioning chips is negligible and the largest contribution is due to disabled modules, as detailed in Table 3. Half of the disabled modules are due to one cooling loop permanently disabled as a result of an inaccessible leak in that loop. Fortunately this affects one quadrant of one of the outermost

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<tr>
<td>Modules</td>
<td>20</td>
<td>10</td>
<td>30</td>
<td>0.73</td>
</tr>
<tr>
<td>Strips</td>
<td>6992</td>
<td>3681</td>
<td>10673</td>
<td>0.07</td>
</tr>
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end-cap disks, and has negligible impact on tracking performance. The remaining modules are predominantly due to on-detector connection issues.

2.2. Data taking efficiency

The SCT Data Acquisition System[6] (DAQ) has proved to be highly reliable with excellent data taking efficiency. There are two potential sources of inefficiency: errors from the ABCD ASICs which flag that data fragments from those chips cannot be used for tracking, and a BUSY signal from the SCT DAQ that prevents ATLAS from taking data.

Chips may flag an error if they become desynchronised from the ATLAS system, or if their configuration becomes corrupt as a result of Single Event Upsets (SEUs). The rate of such errors is very low, but was further minimised in 2010 by the introduction of online monitoring of chip errors in the data and the automatic reconfiguration of the modules with those errors. In 2011, SCT implemented in addition an automatic global reconfiguration of all SCT module chips every \(\sim 30\) minutes as a precaution against subtle deterioration in chip configurations as a result of SEUs. Figure 4 shows the fraction of data links with errors during the 2010 data taking period, which shows that the fraction of data links with errors was typically \(\sim 0.2\%\).

The main component in the SCT DAQ is the Readout Driver Board (ROD), which provides the front-end data flow, data processing and control of up to 48 modules. There are 90 RODs in the SCT DAQ, and if any experience an error condition (such as FIFO overflows), they will exert a BUSY signal to stop ATLAS data taking. In 2010, SCT implemented the mechanism to automatically reconfigure and reintegrate an entire ROD with minimal interruption to ATLAS data taking, in the very rare cases of a ROD BUSY.

2.3. TX Issues

A significant component failure has been the off-detector VCSEL arrays used to transmit the trigger and commands to the modules. The onset of deaths of individual VCSEL channels started several months after SCT operations began, and continued thereafter at a typical rate of three or four deaths per day. VCSEL deaths were originally attributed to inadequate ESD precautions at the point of their manufacture in the factory, and the entire operational stock of VCSELS were replaced in 2009 with units manufactured with improved ESD procedures. However, further TX channel deaths within the newly installed batches were then attributed to degradation arising from exposure of the VCSELS to humidity. In 2011, new TX transmitters from a different manufacturer demonstrated improved robustness against humidity, and all TX transmitters were replaced again with those products after the end of 2011 operations.

Operationally, the impact of the TX deaths was minimised by the ability to use TX redundancy. If TX redundancy was not available (for example, if the module providing the redundancy was also using redundancy itself) then the module was disabled until the opportunity arose to replace the transmitter between data taking periods. The number of modules disabled due to TX deaths and the inability to use redundancy rarely increased above \(\sim 5\).

3. Tracking Performance

3.1. Noise

Calibration of the SCT can be performed via charge injection in the front end of the ABCD. For each channel, a histogram of occupancy as a function of discriminator threshold is created, and a complementary error function fitted. The threshold at which the occupancy is 50% corresponds to the median of the injected charge, while the sigma gives the noise after amplification. To calibrate the discriminator threshold, the DAQ system initiates threshold scans for several different values of injected charge, and a fit to the resulting response curve yields the gain and the input noise for each channel. Typical noise values are shown in Figure 5, which can be compared with a hit threshold from tracks of \(\sim 6000\) electrons. The variation in noise reflects the different module shapes for the barrels and end caps. An alternative method is to determine the noise occupancy from counting the fraction of triggers which result in a hit when there is no particle activity in the detector.
Figure 6: Noise occupancy data for the SCT barrels and end-caps. The dashed line indicates the nominal design limit.

6 shows the measured noise occupancy, which is significantly lower than the design limit of $<5 \times 10^{-4}$.

3.2. Intrinsic Hit Efficiency

The hit efficiency is determined by counting the number of recorded hits on high $P_T$ tracks ($>1\,\text{GeV}/c$) through the SCT by the number of possible hits, after ignoring known disabled strips. The nominal design requirement was for hit efficiency to be $>99\%$. Figure 7 shows the hit efficiency for each side of the 4 barrel layers. The barrels have a mean hit efficiency of 99.9\% over all layers. Similarly, the hit efficiencies for the SCT end-caps are measured to be 99.8\%.

3.3. Lorentz Angle

Charge carriers drifting through the silicon bulk are deflected in the presence of a magnetic field. The number of strips which collect charge from an incident track (the "cluster width") varies with the angle of incidence of the track. The track incidence angle at which the cluster width is minimal is called the Lorentz angle, which is a function of the B-field, depletion voltage and temperature. Figure 8 shows the cluster width as a function of track incidence angle for the barrel layers, indicating the Lorentz angle. Figure 9 shows the measured Lorentz angles for collision data and cosmic ray data for each barrel layer compared with model predictions, which are sensitive to the models of signal digitisation and radiation damage. Measured values are in very good agreement with predictions. Note the different value for Layer 3 is due to an increased operating temperature for this layer.

3.4. Alignment

Alignment in SCT is performed using a track based algorithm which minimises the $\chi^2$ of track hit residuals (defined as the measured hit position minus the expected position from the track extrapolation). Initially, alignment was performed using survey and cosmic ray data, and continues to improve with the
acquisition of high \(P_T\) tracks from collision data. Figure 10 shows the unbiased residual distributions in \(x\) for the barrel layers, comparing the ideal alignment from Monte Carlo predictions with the measured residuals from data in Autumn 2010. The FWHM of measured data was 36\(\mu m\) at that time and can be compared with 42\(\mu m\) measured in May 2010. The improvement with increased data is clear as the measured residuals approach the design value of 34\(\mu m\). The FWHM for end-cap data has already reached 38\(\mu m\) which is the ideal limit for the end-caps from Monte Carlo predictions.

3.5. Mechanical Stability

The SCT is equipped with a Frequency Scanning Interferometry (FSI) system which continuously monitors its mechanical stability. The FSI consists of 842 fibre coupled interferometers which form a geodetic grid of distance measurements between nodes to determine the relative positions and orientations of the barrel cylinders and end-cap disks. The FSI has demonstrated that the SCT is mechanically stable and rigid. In the absence of any environmental changes, the interferometer lengths are very stable at \(\sigma \sim 9\text{nm}\). Typical deviations during a ramp of the solenoid magnet (where the 2T solenoidal field cycles off then on again) result in temporary deviations of up to \(\sim 3\mu m\).

4. Radiation Damage

Significant increases in leakage currents have been observed in the SCT, as expected from bulk damage due to non-ionising radiation. Figure 11 shows the average leakage current for modules from each of the barrel layers measured over a two year period. The leakage current increase correlates closely with the delivered LHC luminosity and temperature cycles, and there is excellent agreement with leakage current predictions derived from the temperature profiles and fluence from FLUKA[7] simulations.

5. Summary

The SCT has been fully operational throughout all ATLAS data taking operations. The detector acceptance and operational efficiency are both excellent, and all tracking performance indicators have met or significantly exceed design specifications. The affects of radiation damage so far match closely the predictions based on the delivered luminosity and temperature history.

References