The ATLAS ITk Strip Detector System for the High Luminosity LHC Upgrade

John Stakely Keller on behalf of the ATLAS Collaboration
Carleton University, 1125 Colonel By Drive, Ottawa, Canada

Abstract
The ATLAS experiment at the Large Hadron Collider is currently preparing major detector upgrades to meet the requirements of the High Luminosity LHC (HL-LHC), scheduled to begin in 2026. With the radiation damage and high density of tracks expected at the HL-LHC, the current Inner Detector would be inoperable, and will instead be replaced by an all-silicon Inner Tracker (ITk). The ITk consists of two sub-systems: a pixel detector close to the beam pipe, and a strip detector at larger radii. This paper presents results from an extensive design and prototyping effort for the ITk strip detector, including studies of the sensors, electronics, and support structures. The performance of the detector has been validated through testbeams, simulations, and thermal and electrical prototypes. Plans for the forthcoming production phase are also presented.

Keywords: HL-HLC, ATLAS, tracker, strip, semiconductor, Silicon

1. Introduction
The ATLAS detector at the Large Hadron Collider has been operating successfully for nearly a decade, recording more than 175 fb$^{-1}$ of pp collision data. From 2024 to 2026, the LHC will be upgraded to the High Luminosity LHC (HL-LHC), which is expected to deliver instantaneous luminosity up to 7.5x10$^{34}$ cm$^{-2}$s$^{-1}$, and an integrated luminosity up to 4 ab$^{-1}$ per experiment. This will provide unprecedented opportunities for precise measurements in the Higgs and electroweak sectors, as well as searches for physics beyond the Standard Model. However, the HL-LHC will also pose significant challenges to the experiments. Most notably, the pileup will reach an average of 200 interactions per bunch crossing, and hadron fluences of up to 2x10$^{16}$ n$_{eq}$/cm$^2$ will cause significant radiation damage.

The current Inner Detector at ATLAS, designed for LHC conditions, would not be able to cope with the high track density or radiation damage of the HL-LHC. Instead, a new detector will be installed, known as the Inner Tracker (ITk). The ITk will be an all-silicon tracker, consisting of a pixel detector [1] in the region closest to the beam pipe, and a strip detector at higher radii [2]. The layout of the ITk is shown in figure 1. The pixel detector consists of five barrel layers, with inclined sensors in the forward region, as well as an endcap with individually located rings. The strip detector consists of four barrel layers and six disks in each endcap. The ITk will extend the tracking coverage of ATLAS from $|\eta| < 2.5$ to $|\eta| < 4.0$. With increased granularity, reduced material, and more radiation-hard sensors, the ITk is expected to maintain or improve the tracking performance in spite of the more challenging environment.

2. Overview of the ITk strip detector

2.1. Sensors and Modules
In order to facilitate a multi-site construction effort, the ITk strip detector employs a highly modular design: the smallest structures (modules) are built into larger structures (staves and petals), which are built into the still larger structures (cylinders and disks) that make up the
detector. The most basic unit, a module, consists of a single strip sensor, one or two flex hybrid circuit boards, and a powerboard. An example of a prototype endcap module is shown in figure 2.

The silicon strip sensors must withstand a hadron fluence of up to $1.6 \times 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$ (including a safety factor of 1.5). For this reason, n$^+$-in-p Float Zone sensors are used [3], which do not suffer from the radiation-induced type inversion seen in n-bulk sensors. Strip isolation is improved using p-stop implants. Extensive irradiation campaigns have been carried out to ensure the sensor performance at the end of lifetime. Figure 3 shows the collected charge as a function of the fluence for different forms of irradiation, measured using beta sources. The results indicate that at the maximum expected fluence an observed signal of more than 10,000 electrons can still be seen. This can be compared to noise values which are expected to be well below 1,000 electrons.

The sensors are 320 $\mu$m thick, and made on 6-inch wafers. Sensors in the barrel are approximately 10 x 10 cm$^2$, while in the endcap, six different annular shapes are used in order to fit the nearly trapezoidal shape of the petals. Sensors contain either two or four rows of strips, with the strip length varying from 18.1 to 60.2 mm, depending on the expected track density. The strip pitch is 75.5 $\mu$m for barrel sensors, while it varies from approximately 70 to 80 $\mu$m in the endcap.

As both sides of support structures are populated with modules, 3-dimensional spacepoint reconstruction is achieved by introducing a stereo rotation between the strips of back-to-back sensors. In the barrel, the sensors are rotated by 26 mrad, for a total stereo angle of 52 mrad. In the endcap, space constraints prevent rotation of the sensors, so the strips themselves are skewed to point slightly off-center, resulting in a stereo angle of 40 mrad.

The hybrid(s) and powerboard are glued directly on top of the sensor to create a module. Hybrids contain a number of front-end ASICs, the ATLAS Binary Chips or ABCstars, as well as one Hybrid Control Chip, or HCC-star [4]. These are described further in the next section. The powerboard contains a DC-DC converter, the radiation-hard bPOL12V, which reduces the input voltage from 11V to 1.5V as needed by the ASICs. The use of a DC-DC converter reduces ohmic losses in the cables allowing a reduction of the material budget [5]. In addition, the powerboard contains an Autonomous Monitoring and Control chip (AMAC), as well as the high voltage bias filter and switch [6].

2.2. ASICs and Electronics

The ABCstar front-end chip is built from 130 nm CMOS technology, and uses analogue amplification and shaping to provide binary output. Each chip has 256 input channels, split among two rows. Data is sent in parallel from each of the ABCstars on one hybrid to the HCCstar, which serves as the interface between the module and the local support. Final versions of the ABCstar and HCCstar chips became available in late 2018. For this reason, most prototyping studies so far, including those in section 3.1, have been done with a previous chipset, the ABC130 and HCC. These chips also used 130 nm technology, but employed a serial readout scheme. Every HCCstar on a stave or petal sends data to an End-of-Substructure (EoS) card, at a rate of up to 640 Mbps. The EoS also sends trigger, timing, and control (TTC) information to the HCCstars at a rate of 120 Mbps. Additionally, detector control signals and bias high voltage are sent from the EoS to the powerboard. The EoS is connected to the modules via a polyimide bus tape. The EoS contains optical transceivers which send data to the off-detector electronics at up to 8.96 Gbit/s [7].
2.3. Support structures

ITk strip modules are glued onto staves and petals in the barrel and endcap, respectively. A schematic of a stave or petal is shown in figure 4. These are low-mass support structures based on carbon fiber honeycomb [8]. Cooling is provided by bi-phase $\text{CO}_2$ circulated through Titanium cooling pipes. The pipes are surrounded by high-conductivity thermal foam. The bus tapes providing electrical connection between the modules and the EoS card are co-cured to the core.

The staves and petals are designed to be inserted into the global support structures, cylinders and disks. This allows testing of the multi-module components prior to assembly into the largest structures. The global supports are made primarily from carbon fiber, to reduce material while providing support across a wide temperature range. All services are connected at the end of the structures, to reduce the material budget inside the tracker.

3. Prototyping studies

3.1. Testbeams

The performance of prototype modules has been extensively studied using high energy testbeam facilities. Two such campaigns were undertaken in 2018, using 4 GeV electrons from the DESY-II facility, and 120 GeV pions from the CERN SPS facility. At DESY, the first long-strip barrel modules were tested, as was the first double-sided endcap structure (two-modules glued on either side of a carbon fiber core). At CERN, an endcap module including a powerboard was irradiated to a fluence of $1.63 \times 10^{15}$ $n_{eq}/cm^2$ at the PS-IRRAD facility, and subsequently tested at the SPS testbeam facility. In both cases, tracking was provided by an EUDET-style telescope [9], with reconstruction performed using the EUtelscope framework [10].

Both the long-strip modules and the double-sided endcap structure showed good performance, with a wide range of operating thresholds showing efficiency above 99% and noise occupancy below 0.1%. This indicates the possibility for excellent tracking at the beginning of lifetime. Additionally, having two modules back-to-back allows for a demonstration of the 3-D spacepoint reconstruction using the stereo angle. Figure 5 shows the residuals for the reconstructed track position in the direction along the strips, using the stereo rotation in a double-sided endcap structure [11].

3.2. Thermal studies

The thermal properties of staves and petals at the end of lifetime have been evaluated using Finite Element Analysis (FEA). These studies indicate that thermal runaway can be avoided if the evaporation temperature of the CO$_2$ is less than -15 C. As the baseline temperature is -35 C, the studies show significant headroom.

The FEA simulations have been validated using thermo-mechanical prototype staves and petals. These prototypes have real carbon cores, but plain silicon “sensors” and heating elements to mimic the ASICs. They are painted black to ensure constant emissivity. Figure 6 shows the measured temperature on a thermo-mechanical stave. The fourth module from the left has been intentionally disabled.

3.3. Electrical staves

The first fully populated staves have recently been constructed. The first of these, shown in figure 7, uses a combination of real modules and “dummy” modules (built with plain silicon instead of real sensors), while the second...
Figure 7: Semi-electrical stave using two real modules and 11 dummy modules.

is made entirely from real modules. An increase in noise of around 10% is seen in the modules after placing them on the stave. Nonetheless, the noise remains acceptably low. The next steps include moving to the final chip set, multi-stave systems tests, and a semi-electrical petal (using a combination of real and dummy modules).

4. Preparations for production

With nearly all design decisions made, and excellent performance demonstrated in many areas, the project is transitioning from the research and development phase into the production phase. In total, 392 staves and 384 petals must be constructed, comprising 17888 modules. The effort will be shared among many institutes across the globe. Pre-production will begin in summer 2019, where 5% of the material will be delivered to establish procedures and qualify construction sites. Particular attention is paid to the quality control and quality assurance procedures, ensuring a highly-functioning detector. This phase will be followed by production, which will last until 2024. At this point the detector will be installed and commissioned in preparation for collision data in 2026.

5. Conclusion

The ATLAS ITk strip detector will be required to provide high tracking performance in the harsh environment of the HL-LHC, even after significant radiation damage. A major research and development program has resulted in a detector design that is capable of meeting these challenging requirements. This has been demonstrated in recent prototype studies, including testbeams, thermal simulations and prototypes, and fully populated staves. These efforts will continue in the near future, in particular incorporating the final set of ASICs. In parallel much of the focus is shifting to preparations for production, which will last from 2019 until 2024 and involve a large, multi-national effort.

6. Acknowledgements

Some of the measurements leading to these results have been performed at the Test Beam Facility at DESY Hamburg (Germany), a member of the Helmholtz Association (HGF); and at the CERN SPS Test Beam Facility. We gratefully thank the operators of these facilities. This project has received funding from the European Union’s Horizon 2020 research and innovation programme under grant agreement No 654168.

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